

Notice of References Cited	Application/Control No. 09/739,758	Applicant(s)/Patent Under Reexamination WATANABE ET AL.	
	Examiner Joseph P. Hirl	Art Unit 2129	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
	B	US-			
	C	US-			
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FOREIGN PATENT DOCUMENTS

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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Scott Griffith et al, A 2K Byte Fully-Associative Cache Memory with On-Chip Dram Control, 15-18 May 1989, IEEE, 10.2.1-10.2.5
	V	
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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